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... Figure 6 identifies **OUTN** and **OUTP** for the differential output pins driven by Q8 and Q7. Also, observe that **INN** and **INP** are internal differential inputs ...

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VGA001 - Variable Gain Amplifier

... LVSS. Ground. IREF25u. Reference Current. CNTRL. **Gain** Control Voltage. **INP**. Positive Input. **INN**. Negative Input. **OUTP**. Positive Output. **OUTN**. Negative Output. ...

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... **lnp Inn Outn Outp** Fig. ... lnp Qinp Qinn linn outn outp C R1 R2 Following the limiter ... consists of a channel selection LPF and a **variable gain amplifier** (VGA) with ...

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... VAROUTR 32 O **Variable** output for right channel audio ... MAX UNIT | VOS | Class-D Output offset voltage (measured differentially) **INN** and **INP** connected together ...

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United States Patent Application: 0010033191

... 2. Voltages comp(V.sub.**INP**) and comp(V.sub.**INN**) are the ... The voltages across the two capacitors, comp(v.sub.**OUTP**) and comp(v.sub.**OUTN**), are the output ...

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